



8/28

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Attorney Docket No: 40296-0003

In re patent application of:
Seon Yong Cha

Confirmation No.: 8794

Serial No.: 10/608,195

Art Unit: 2811

Filed: June 30, 2003

Examiner: Ori Nadav

For: NAND-TYPE MAGNETORESISTIVE RAM

RESPONSE TO RESTRICTION REQUIREMENT

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450


Sir:

In response to the Office Action mailed September 1, 2004 (Paper No./Mail Date 0), applicant hereby elects without traverse Group I, Claims 1-7 as depicted in Figure 3, an electronic circuit, for prosecution in the subject application. Applicant, of course, reserves the right to file one or more divisional applications covering the subject matter of the non-elected claims.

It is respectfully submitted that the application is in condition for examination, and an early action on the merits is courteously requested.

Respectfully submitted,

Date: 9/28/04


52947

Heller Ehrman White & McAuliffe LLP
1666 K Street, N.W., Suite 300
Washington, D.C. 20006-4004
Telephone: (202) 912-2000
Facsimile: (202) 912-2020

for/ Johnny A. Kumar
Attorney for Applicant
Reg. No.: 34,649

Customer No. 26633